

15.20/533

*Disc case.  
#10/A  
1/14/03  
3/25/03*

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:	)	Group Art Unit: 2822
MARUO	)	
Serial No. 09/687,197 ✓	)	Examiner: Duong, K.
Filed: October 14, 2000	)	
For: SEMICONDUCTOR DEVICES	)	
AND METHODS FOR	)	
MANUFACTURING THE SAME	)	

AMENDMENT

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sirs:

In response to the Office Action dated September 11, 2002, the response being due by March 11, 2003 by the enclosed petition for extension of time, please enter and consider the following.

IN THE TITLE:

Please delete the title and insert the following in its place:

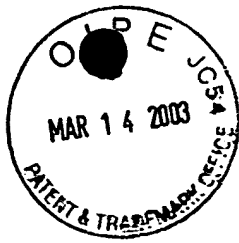
--Methods for Manufacturing a Semiconductor Device Including a Trench Isolation Region--

IN THE CLAIMS:

Please cancel claims 11-25 without prejudice.

Please amend claim 1 as follows:

- A1*
1. (amended) A method for manufacturing a semiconductor device having a trench element isolation region including a trench and a trench insulating layer that fills the trench, the method comprising the steps of:
- (A) forming a polishing stopper layer over a substrate, the polishing stopper layer having a predetermined pattern for a chemical-mechanical polishing;
  - (B) removing a part of the substrate using a mask layer including at least the polishing



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ASSISTANT COMMISSIONER OF PATENTS  
Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- ☐ No additional fee is required.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO PREVIOUSLY PAID FOR		PRESENT EXTRA RATE		ADDIT. FEE	OR	RATE		ADDIT. FEE
TOTAL	20	MINUS	26	=	0	x	\$0	OR	x 18		\$0
INDEP CLAIMS *	4	MINUS	4	=	0	x	\$0	OR	x 84		\$0
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM						+	\$	OR	+ 280		\$0
					TOTAL		\$0	OR	TOTAL \$0		

☐ Please charge Deposit Account No. 50-0585 the amount of \$\_\_\_\_\_ to cover the extension fee and also the amount of \$\_\_\_\_\_ to cover the claim fee. A duplicate copy of this sheet is enclosed.

- ☒ A check in the amount of \$ 930 to cover the extension fee is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee for additional claims is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the RCE fee is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the Information Disclosure Statement fee is enclosed.

☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. No. 50-0585. A duplicate of this sheet is enclosed.

- ☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

*Alan S. Raynes*  
Alan S. Raynes

Dated: March 7, 2003

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**Certificate of Mailing**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on March 7, 2003.

*Alan S. Raynes*  
Alan S. Raynes

*March 7, 2003*  
Date